

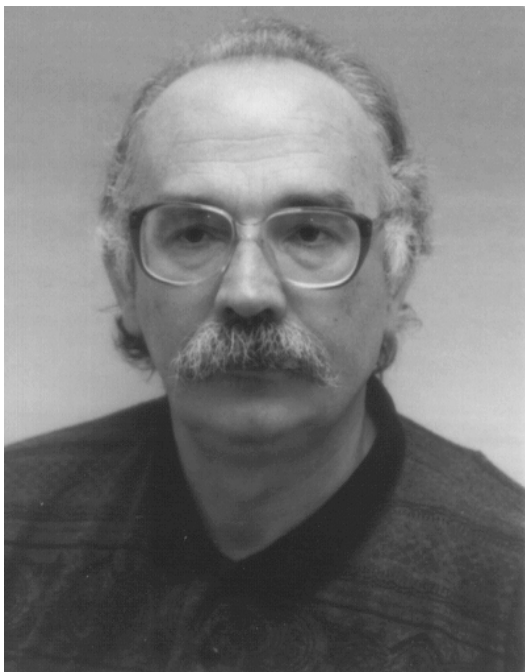
Vančo B. Litovski

Professional biography

1970.-2014.

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Prof. Vančo B. Litovski

<http://leda.elfak.ni.ac.rs>

1. Short biography

Prof. Litovski was born in 1947 in Rakita, South Macedonia, Greece. Primary school and „gymnasium“ he graduated in Bitola, Republic of Macedonia. He enrolled the Faculty of Electronic Engineering in Niš in 1965 where he graduated in March 1970. He was appointed teaching assistant at the Chair of Electronics at the Faculty of Electronic Engineering in Niš on March 20, 1970. He got his Magisterium in June 1974. He served his one year obligatory military service in 1974/75. He got his Ph.D. in June 1977; He was elected a full professor at the Faculty of Electronic Engineering in Niš in 1987 he was appointed visiting professor at the University of Southampton, UK, on November 1999. He was performing the duty of head of the Chair of Electronics at The Faculty of Electronic Engineering in a period of 12 years. He was teaching the following subjects “Electronics I“, “Design of electronic circuits“, “Physical bases of electronics“, “Amplifiers“, “Testing of Electronic circuits“, “Neural networks” and “System on chip design”. He was teaching also at the Universities of Priština, Sarajevo, Novi Sad, and Banja Luka.

As an expert he was serving for several years as a consultant for research and development of the CEO of “Elektronska Industrija Niš“.

Prof. Litovski was member “The Institute of Electrical and Electronic Engi-

editorial board of the journal „Electronics“ published by the Faculty of Electrical Engineering in Banja Luka.

Prof. Litovski proudly claims that he was the one who brought to the Faculty of Electronic Engineering: the computer graphics, the Unix operating system, the simulation of electronic circuits and systems, the design of electronic integrated circuits, The TCP-IP protocol, the supercomputing in Beowulf technology, the neural networks, and he was the first to introduce NIDAQ-LabView technology in laboratory teaching at the Faculty.

He was the first to establish a research laboratory, LEDA, at the Faculty. In: Stephan Pacall (Advisor, Directorate C, »Lisbon strategy and policies for information society«), »Serbia – ICT RTD technological audit«, published by the European Commission Information Society and Media, on March 2010, LEDA was identified as one among 17 centers of excellence of Research and development in Serbia.

<http://ebookbrowse.com/serbia-ict-rtd-technological-audit-final-report-pdf-d115707490>

2. Scientific activities

The scientific opus of Prof. Litovskog is mainly related to design of electronic circuits and systems (discrete and integrated). Being a pioneer in the field he practically paved the research road for research in the subject in Serbia. In his earliest research phase he was investigating computer-aided synthesis of electronic communication filters. He made his doctoral thesis in that field while his results were published in the most distinguished journals in the USA and Yugoslavia. Together with his mentor Prof. Branko Raković he introduced a new class of filtering functions named Least-Squares Monotonic (LSM). Toward the end of the seventies of the twentieth century, he started his research in integrated circuits design. The research work was performed within the Laboratory for electronic design automation (LEDA). In the field of CAD of electronic circuit thanks to his personal efforts and to efforts coordinated by him, the first Yugoslav electronic-circuit simulators were developed (named LIFT and MOST) in the early eighties. After that this research task was further fostered so that LEDA became a leading research center in the field. Software packages for simulation mixed-signal and mixed-level described circuit and systems developed in LEDA were implemented at several universities in Western Europe.

Automation of IC layout design was the next activity undertaken within LEDA. The first Yugoslav integrated software package for gate-array design named ISPGM was developed and implemented. It was presented as an invited lecture at the »3rd MidEuropean Custom Circuit Conference, in Sopron, 1991“. This package was directly used in the Nis Elektronska Industrija for

design CMOS gate arrays. Based on these results decisions were made at the federal level for investments into CAD equipment for electronic design.

Short name	Research unit	NoE	NoR	Expertise by FP7-ICT Challenge and Objective										Total	CC1	CSR [%]	CCR [%]
				1	2	3	4	5	6	7	FET						
(1)	(2)	(3)	(4)	(5)	(6)	(7)	(8)	(9)	(10)	(11)	(12)	(13)	(14)	(15)	(16)		
FON.2	FACULTY OF ORGANIZATIONAL SCIENCES (FOS), UoB. GOOD OLD AI	100	20	[1.2] [1.3] [1.6]	(2.1) (2.2)	-	[4.1] [4.2] [4.3]	-	-	[7.2]	-	9	2,3	3,5	3,7		
ETF.4	SCHOOL OF ELECTRICAL ENGINEERING, UoB. Chair Of Computer Engineering and Information Theory	24	24	[1.2] [1.3]	-	-	[4.2] [4.3]	[1.1] [1.2]	[6.1] [6.2]	[7.3]	-	9	1,8	3,5	3,3		
FTN.1	FACULTY OF TECHNICAL SCIENCES, University of Novi Sad Chair of Communications and Signal Processing	25	24	-	(2.1) (2.2)	-	[4.3]	-	-	[7.1] [7.2]	2	7	1,8	2,8	3,4		
PMF	FACULTY OF MATHEMATICS UNIVERSITY OF BELGRADE Department of Computing and Informatics	35	21	[1.1] [1.2] [1.3]	(2.2)	-	[4.1] [4.2] [4.3]	-	-	-	-	7	2,3	2,8	4,0		
ELFAK.1	Faculty of Electronic Engineering, University of Niš, Laboratory for Electronic Design Automation (LEDA)	12	11	-	-	[3.2] [3.4]	[4.2]	-	[6.3] [6.5]	-	1	6	1,5	2,4	1,3		
IMTEL	Institute for Microwave Techniques and Electronics (IMTEL)	49	22	[1.6]	(2.1)	[3.4] [3.5] [3.9]	-	-	-	-	-	5	1,7	2,0	3,0		
ETF.3	SCHOOL OF ELECTRICAL ENGINEERING, UoB. Chair of General Electrical Engineering	13	13	[1.6]	-	[3.2] [3.9]	-	-	[6.2] [6.4]	-	-	5	1,7	2,0	1,8		
ELFAK.2	Faculty of Electronic Engineering, University of Niš Chair Of Telecommunications	26	26	-	(2.1) (2.2)	[3.4]	-	-	[6.2]	-	-	4	1,3	1,6	2,8		
IRITEL	IRITEL AD BEOGRAD	195	85	[1.1]	-	[3.4] [3.5]	-	-	-	-	-	3	1,5	1,2	10,4		
DKTS	PUPIN TELECOM DKTS	165	40	-	-	[3.4]	-	-	[6.3] [6.5]	-	-	3	1,5	1,2	4,9		
	TOTAL	1368	730	30	11	28	18	7	33	12	18	157					

Legend:

- 1 - Pervasive and Trustworthy Network and Service Infrastructures
- 2 - Cognitive Systems, Interaction, Robotics
- 3 - Components, systems, engineering
- 4 - Digital Libraries and Content
- 5 - Towards sustainable and personalized healthcare
- 6 - ICT for Mobility
- 7 - ICT for Independent Living, Inclusion and Governance
- FET - Future and Emerging Technologies

Prof. Litovski started research in electronic testing and design for testability in Serbia. The later is especially related to the introduction of the IEEE 1149.1 standard. His main research results in this area are related to establishment of methodology for fault modelling, fault simulation and its implementation within the system for automatic test pattern generation for analog and digital circuits. Recently he introduced electronic circuit diagnostic as a research subject in Serbia. He published the first textbook on the subject of testing and diagnosis of electronic circuits in Serbian.

Implementation of artificial neural network in computer-aided design of electronic circuits and systems was a research subject where LEDA and Prof. Litovski gave a significant scientific contribution to the overall research efforts. The first international meeting on ANNs took place at the Faculty of Electronic Engineering in the year 1990. Prof. Litovski was the first to implement ANNs for electronic device modelling. In that way he opened a completely new way of black-box modelling of electronic components and circuits. The importance of these results was broadly recognized. That may be confirmed by the fact that the British EPSRC granted a research project on this subject to Prof. Litovski in

the war year of 1999/2000.

Prof. Litovski was the first in Serbia to introduce research in the field of sustainable electronic design. His social engagement in the subject helped seriously to the recognition of the problem of the electronic waste and the need for sustainable and eco-electronic design in the Serbian community.

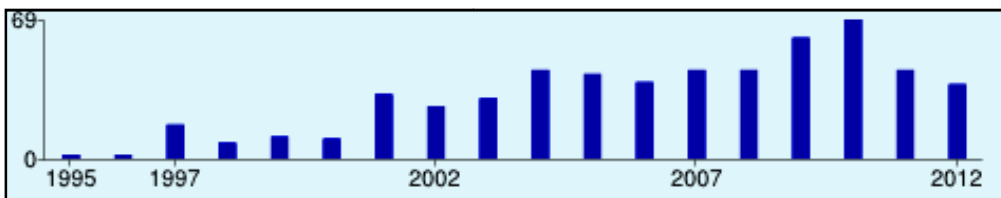
To his name is connected the implementation of ANNs for prediction based on short time series. These concepts were implemented for prediction in various fields such as electricity loads prediction, production of electrical energy, production of microelectronic components, prediction of technological developments in electronics, prediction in eco-developments etc.

Prof. Litovski published several hundreds of publications as can be seen from the lists below. He had 90 coauthors while the average number of authors per publication on his publications was around 2.7.

3. Citations

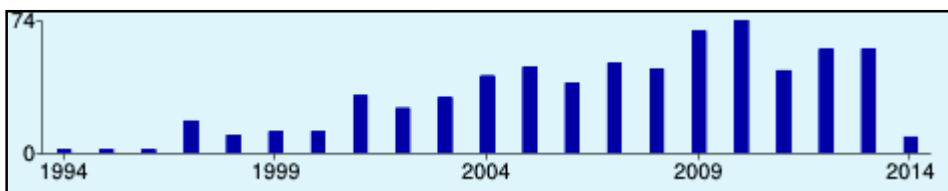
Here is a histogram created by Scholar Google containing all citations available to them with no selections. It is accurate but not precise and it covers a short period only.

<http://scholar.google.com/citations?user=Z5IhjdYAAAAJ&hl=sr>



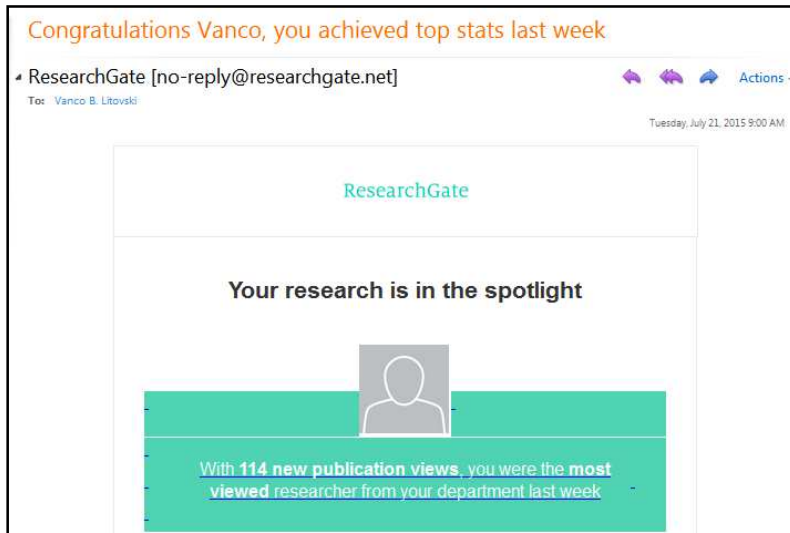
Citation indexes on March 21, 2014

	All	Since 2009
Citations	716	313
h-index	12	8
i10-index	19	5



Research gate has its own measure of V. Litovski's citation. On the same

date as above it was: Your RG Score: 18.66. On July 21, 2015 (almost three years after retirement) Researchgate published the message below:



Next, a list of citations will be given with comments when appropriate. *In that list no self citations and citations by Prof. Litovski's coauthors are mentioned.*

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Sangiovanni-Vincentelli, and M.R. Prasad, "Routing Techniques for Deep Sub-micron Technologies", EECS Department **University of California, Berkeley** Technical Report No. UCB/ERL M99/15 1999 (<http://www.eecs.berkeley.edu/Pubs/TechRpts/1999/3602.html>), it is written: "**Area routing techniques have been reviewed as part of several surveys on physical design [KO90, Oht86, DL89]**". With [DL89] the above was cited.

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Soubhik Sarkar

Department of Electronics

Carleton University

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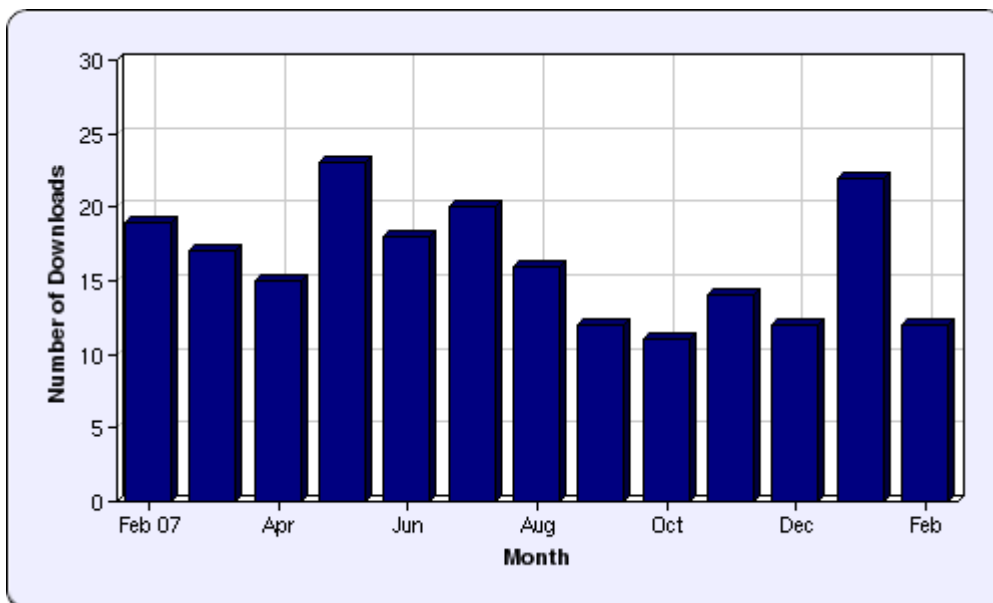
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To probe further

For background reading, a textbook that starts at the basics and covers the basic solution methods used in most modern CAD capabilities is *VLSI Circuit Simulation and Optimization*, by V. Litovski and M. Zwolinski (Kluwer Academic, Dordrecht, the Netherlands, 1997).

About the author

Barbara Chappell is a principal engineer with Intel Corp.'s Design Technology Department within the Microprocessor Products Group in Hillsboro, Ore., where she has been since 1995. For 17 years prior to that, she was a member of the research staff at IBM Corp.'s Thomas J. Watson Research Center, Yorktown Heights, N.Y. Chappell has produced more than 24 publications and has been issued more than 18 U.S. patents.

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4. Engineering engagement

The main research subject of Prof. Litovski's, computer aided design of electronic circuits, was chosen thanks to the initiative of "Elektronska industrija" and the Ministry (in those times Community) of Science of Serbia in the year 1978. Since then, being supported by Elektronske industrije, The Regional Community of Science in Niš, The Serbian Ministry of Science, The Serbian Council for international scientific collaboration, the Yugoslav peoples army, and the Faculty of Electronic Engineering in Niš, a line of successful research may be followed. For example, in the year 1997, only two years after the lift of the international sanctions against Yugoslavia, in, under the leadership of Prof. Litovski three integrated circuits were designed and samples produced. While not explicitly pronounced, all three chips were intended to be built in domestic products. One should consider these results as an important contribution to the regeneration of this kind of knowledge in Serbia after the lifting of the sanctions.

To the name of Prof. Litovski is connected the design of the first Serbian custom integrated circuit in the year 1990, that was designed in collaboration of "Ei Mikroelektronika" from Niš, "Rudi Čajavec" from Banja Luka and the Faculty of Electronic Engineering in Niš. It was completely produced in Serbia including the silicon crystal. Similarly, the first mixed-signal integrated circuit

in Serbia was designed in LEDA in collaboration with the Middlesex University from London, England in the year 1992.

The private consultant William Lurie, from Boca Raton-a (21061 Cottonwood Drive), Florida 33428, USA, in the early eighties of the past century, developed and produced electrical telecommunication filters based on the published designs of Prof. Litovski.

Prof Litovski was leading several research and educational project financed by the Yugoslav and Serbian Government and by several European authorities. Following is the list of project in reverse time order:

Financed by domestic resources

2011- ...“Advanced technologies for measurement, control, and communication on the electric grid”

2004-2007 “A system for power factor measurement and correction of electronic equipment”, funded by the Ministry of Science of Serbia (code 232014).

2002-2004 “Design testing and eco-design of electronic circuits and systems” funded by the Ministry of Science of Serbia (code IT.1.02.0075.A).

1996-2000 “Integrated Circuit Design Automation”. Funded by the Ministry of Science of Serbia

1994-1996 “Complex Microelectronic Devices Design”. Funded by the Ministry of Science of Serbia

1991-1995 “Development of System for IC Design and Verification” funded by the Ministry of Science of Serbia.

1991-1994 “Software development for design automation of Application specific CMOS integrated circuits“, funded by Yugoslav Federal Ministry of Science.

1986-1990 “Semiconductor Microelectronics and Optoelectronics” funded by the Ministry of Science of Serbia

1981-1985 “Microelectronics Devices” funded by the Ministry of Science of Serbia

Financed by foreign resources

2015-2016 The SYNAPS project, Realized by the Dept. of Electronics and Electrical Eng. at the University of Bath, UK, Project code RE-EE1107.

2010-2011 The ISSNBS project, Realized within the Pact of Stability of Southeast Europe and funded by The German Government (DAAD).

2008-2010 “South-Eastern European GRID eInfrastructure for regional eScience-SEE-GRID-SCI” Specific Support Action SEE-GREID-SCI, Funded by the Commission of the European Community, Information Society and media Directorate-General.

2006-2008 “South-Eastern European Grid-Enabled eInfrastructure

Development 2” Specific Support Action SEE-GREID-2, Funded by the Commission of the European Community, Information Society and media Directorate-General.

2007-2008 “System on Chip design”, funded by TEMPUS JEP-41107-2006

2001-2007 ISSNB, Realized within the Pact of Stability of Southeast Europe and funded by The German Government (DAAD).

2005-2007 “Electronic education in Serbia”, TEMPUS CD_JEP.17028.2002.

2005-2007 “Course Development Program +”, which is being implemented by WUS Austria within its program “Support to Higher Education in Bosnia and Herzegovina in 2005/ 2007” (No: 7967-00/2005)

2000-2002 Researcher on a set of small projects with Middlesex University, London (“Low bit High Speed BOSA DSM”, “Evaluation and Design of a High Frequency Low Pass Oversampling DAC” i “Band Pass Oversampling AD Converter”).

1999-2000 Research project realized with the University of Southampton funded by the British EPSRC (Grant no. GR/M85531, 02 July 1999).

1995-1995 A research project realized with the University of Southampton funded by the British EPSRC. (Grant ref. No. GR/K54129, 12 June 2005).

1989-1991 A research project realized with the University of Southampton funded by the British Council (Alis No. 245, Belgrade).

4.a Other professional activities

1. Senior member of the “Serbian Academy of Engineering Sciences”, Belgrade
2. Founder and the first president of Yugoslav Simulation Society.
3. Member of IEEE.
4. Member of the presidency of ETRAN
5. President of the Organizing Committee of the Small Systems Simulation Symposium held at the University of Niš.
6. Editor of the Proceedings of the “Small Systems Simulation Symposium”
7. Founder and first editor of the journal "Facta Universitatis, Series: Electronics and Energetics" published by the University of Niš.
8. Currently : Representative of Serbia at the PAB (Public Authority Board) of the Joint Undertaking ARTEMIS with the EC Commission
9. Currently: President of the Coordinating Council for ICT at the regional Chamber of Commerce in Niš.
10. Member of Editorial Board: The Journal Electronics;
11. Member of the steering committees of: Conf. TELSIXS, MIEL, INDEL, SSSS, NEUREL.

12. As an expert, by appointment No. AL00065673, of the EC (Information Society and Media / Embedded Systems and Control) I am currently reviewer of the Moby-Dic scientific project (No. 248858) financed within the FP7.
13. President of the Board of the Nis Cluster of Advanced Technologies - NiCAT
14. Reviewer for: IEEE CAS; IEEE TCAD II; Microelectronics Reliability; IEE Proceedings; IEEE CAD of ICAS; Int. J. of Electronics; J. of Circuits Systems and Computers; Int. J. of Information technology; Serbian J. of Electrical Engineering; COMPEL; IEEE ISCAS (symp.); ETRAN (Conf.); IASTED (Conf.); TELFOR (Conf.); TELSIS (Conf.); SSSS (Conf.), AFRICON 2013 (Conf.), ICT Innovation 2013 (Conf.), ICMRA 2013 (Conf.), PEMC 2014, (Int. power electronics and motion control Conf. and exhibition).
15. Guest editor of the „Electronics ISSN 1450-5843“, Vol. 16, No. 1, June 2012.

4.b Awards

- The Savastano Award for 1998 (Federation of the European Simulation Societies)
- The Tesla Award for 1994 (The Tesla Foundation)
- The ETRAN Award for 1986 (The Association for ETRAN)
- A long list of recognitions by the Faculty of Electronic Engineering, University of Niš, The Town of Niš, The University of Banja Luka, and the University of Eastern Sarajevo.

5. LIST OF PUBLICATIONS(Papers written in Serbian are marked by an asterix)

5.a Monographs

- a.1.***Litovski, V.**, *"Automatizacija projektovanja integrisanih kola"*, u "MIPRO'88 Mikroelektronički sklopovi-principi rada i projektiranja", pod redakcijom prof. Petra Biljanovića, Rijeka, Maj, 1988. (Serbian)
- a.2.***Litovski, V.**, *"Logička simulacija"*, u monografiji: "Projektovanje VLSI, I deo", Nauka, Beograd, 1991, pp. 106-181.
- a.3.* Radenković, T., Radenković, Z., **Litovski, V.**, *"Integrirani softverski paket za projektovanje gejtovskih matrica (ISPGM)"*, u monografiji "Projektovanje VLSI, I deo", Nauka, Beograd, 1991, pp. 411-481. (Serbian)
- a.4. Andrejević Stošović, M., **Litovski, V.**, *"ANN Application to Modelling of the D/A and A/D Interface for Mixed-Mode Behavioural Simulation"*,

Micro Electronic and Mechanical Systems, Edited by Kenichi Takahata, Intech, ISBN 978-953-307-027-8, 2009, pp. 369-384.

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a.6. Milić, M., **Litovski, V.**, "*New concepts of asynchronous circuits worst-case delay and yield estimation*", Micro Electronic and Mechanical Systems, Edited by Kenichi Takahata, Intech, ISBN 978-953-307-027, 2009, pp. 455-476

b. Invited papers published in international journals

b.1. Damnjanović, M., **Litovski, V.**, "*A Survey of Routing Algorithms in Custom IC Design*", J. of Semicustom ICs, (ISSN 0026-2692, Microelectronics Journal (Incorporating Journal Of Semicustom Ics)), Vol. 7, No.2, December, 1989, pp. 10-19.

c. Paper published in international journals

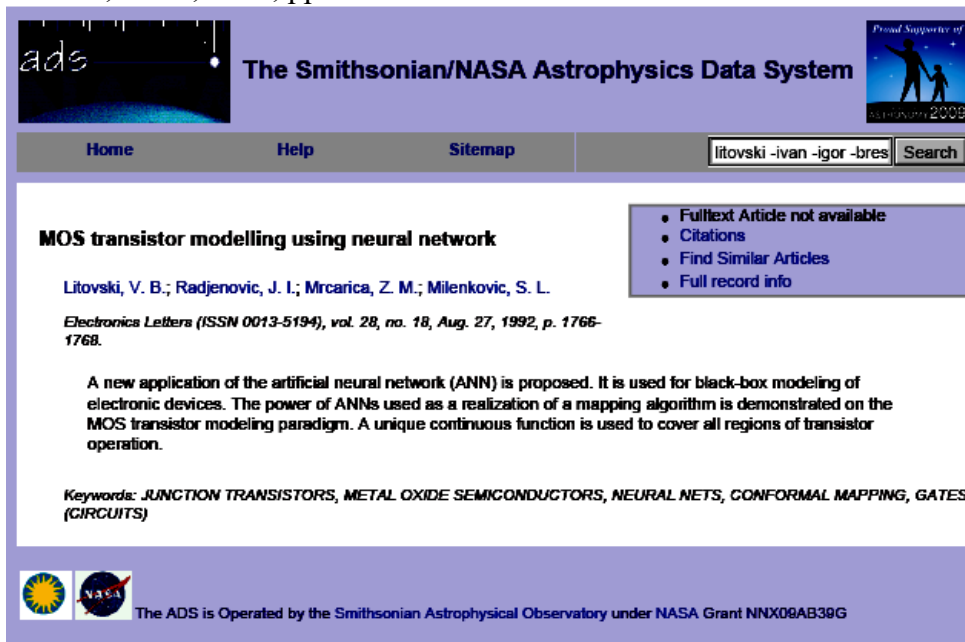
- c.1. Raković, B.D., **Litovski, V.**, "*Least-Squares Monotonic Lowpass Filters with Sharp Cutoff*", *Electronic Letters*, ISSN: 0013-5194, Vol. 9, No. 4, 1973, pp. 75-76.
- c.2. Raković, B.D., **Litovski, V.**, "*Monotonic Passband Low-Pass Filter With Chebyshev Stopband Attenuation*", *IEEE Trans. on Acoustics, speech, and signal processing*, ISSN: 0096-3518, Vol. ASSP-22, 1974, pp. 39-44.
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The screenshot shows the ADS website interface. At the top, there is a navigation bar with links for Home, Help, and Sitemap. A search box contains the text "litovski-ivan-igor-bres" and a Search button. The main content area displays a search result for the paper "MOS transistor modelling using neural network" by Litovski, V. B.; Radjenovic, J. I.; Mrčarica, Z. M.; Milenkovic, S. L. The abstract describes a new application of an artificial neural network (ANN) for black-box modeling of MOS transistors. The keywords listed are JUNCTION TRANSISTORS, METAL OXIDE SEMICONDUCTORS, NEURAL NETS, CONFORMAL MAPPING, GATES (CIRCUITS). A sidebar on the right offers options: Fulltext Article not available, Citations, Find Similar Articles, and Full record info. The footer includes logos for the Smithsonian Astrophysical Observatory and NASA, along with the text "The ADS is Operated by the Smithsonian Astrophysical Observatory under NASA Grant NNX09AB39G".

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URL: <http://ieeexplore.ieee.org/stamp/stamp.jsp?tp=&arnumber=6870441&isnumber=6914631>

To:

Vančo B. Litovski

Tuesday, August 04, 2015 11:03 PM



Dear Prof. Litovski,

Thank you for publishing with us. Your article, [Standard Cell-Based Low Power Embedded Controller Design](#), is an important contribution to the [Journal of Circuits, Systems and Computers](#) and its subject area.

We hope to have the privilege of reviewing and publishing your next article.

Sincerely,
Journal Department
World Scientific Publishing

c.79. Mirković, D., Petković, P., and **Litovski, V.B.**, "A second order s-to-z

- transform and its implementation to IIR filter design*", COMPEL: The Int. J. for Computation and Mathematics in Electrical and Electronic Eng., Vol. 33, No. 5, 2014 pp. 1831-1843.
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d. Paper published i domestic journals in Serbian(marked by asterix) and English

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5.e. Invited papers published at international conferences

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- e.3. **Litovski, V.**, “*New Results In Integrated Software Development For Gate-Array Design*”, Proc. of the 3rd MidEuropean Custom Circuit Conf., Sopron, 21-24 April, 1991, pp. 69-83. Invited paper
- e.4. **Litovski, V.**, “*New Tehnologies in theDesign of Integrated Electronic Circuits*”, 2nd Serbian Conf. on Microelectronics and Optoelectronics, MIOPEL '93, Niš, 26-28 Oktobar, 1993, pp. 383-399. Invited paper
- e.5. Zwolinski, M., **Litovski, V.**, “*Hybrid Simulation: The State-Of-The-Art*”, 2nd Serbian Conf. on Microelectronics and Optoelectronics, MIOPEL '93, Niš, Srbija, 26-28 Oktobar, 1993, pp. 375-382. Invited paper
- e.6. Milenković, S., **Litovski, V.**, Obradović, Z., “*A New Adaptive Move Type Selection In Simulated Annealing*”, 15-16 Int. Anual School on Semiconductor and Hybrid Technologies, 1992-93, Sozopol, May, 1994, pp. 22-31. Invited paper
- e.7. Pantić, D., Milenković, S., Trajković, T., **Litovski, V.**, Stojadinović, N., “*Inverse Modeling Of Semiconductor Manufacturing Processes*”, 20th Int. Conf. on Microelectronics MIEL '95, Vol. 1, Niš, 12-14 Sep., 1995, pp. 321-326. Invited paper

- e.8. **Litovski, V.**, Randelović, Z., Damnjanović, M., "Routing In Standard Cells", 20th Int.Conf. on Microelectronics MIEL '95, Vol. 2, Niš, 12-14 Sep., 1995, pp. 461-466. Invited paper
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- e.10. Andrejević, M., **Litovski, V.**, "Electronic Modelling Using ANNs For Analogue and Mixed-Mode Behavioural Simulation", Proc. of NEUREL 2002, 2002, Belgrade, pp. 113-118. Invited paper
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- e.12. Dimitrijević, M., Milojković, J., Bojanić, S., and **Litovski, V.**, "ICT and Power: Synergy and Hostility", Proc. of the 10th Int. Conf. on Telecommunications in Modern Satellite, Cable and Broadcasting Services, TELSIS 2011, Niš, Serbia, ISBN: 978-1-4577-2017-8, pp. 186-194, Invited paper.

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<ul style="list-style-type: none">Smart Grid NewsletterSmart Grid Publications from IEEE Xplore→ Smart Grid→ Photovoltaics→ Renewable Energy→ Plug-in Hybrid Electric VehicleStandards Education E MagazineInteractive Search ToolIEEE XploreTechNav	<h3>ICT and power: Synergy and hostility</h3> <p>An attempt is made in this paper to summarize the state of the art in the interaction between the ICT (information communication technologies) that becomes ubiquitous and the electrical power production and distribution. Both are considered to have a difficult task to fulfill enormous rise of demand that is becoming above all expectations. In the same time they technologically interfere in the sense that they mutually help in the fulfillment of their main task while in the same time loading each other with problems inherent to the respective technology. In the paper we will try to merge our (LEDA laboratory of the University of Niš) own results with ones available in the literature in order to give as complete a picture of the subject as possible.</p> <p><i>Dimitrijevic, M.; Milojkovic, J.; Bojanic, S.; Litovski, V.</i></p> <p><input type="button" value="Access Now"/></p> <p>This paper appears in: Telecommunication in Modern Satellite Cable and Broadcasting Services (TELSIS), 2011 10th International Conference on Issue Date: 2011 On page(s): 186 - 195 ISSN: Print ISBN: 978-1-4577-2018-5 Digital Object Identifier: 10.1109/TELSIS.2011.6112031</p>
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- e.13. Andrejević Stošović, M., Lukač, D., and **Litovski, V.**, „*Modeling and circuit simulation of photovoltaic cells – an overview*“, 7th Int. Symp. „Nikola Tesla“, Belgrade, Serbia, Nov. 2011, pp. 83-92. ISBN 987-86-7466-420-9.
- e.14 Le Blond, S., and Litovski, V. B., “A mechanism for collaboration between industry and academia in the UK, exemplified by project SYNAPS”, Proc. of the 6th Small Systems Simulation Symposium, SSSS'14, Niš, 2016, ISBN 978-86-6125-154-2, pp. 1-7

5.f. Invited papers published at domestic conferences(Papers written in Serbian are marked by an asterix)

- f.1.* Zarković, K., Milenković, S., Glozić, D., **Litovski, V.**, "*Novi rezultati u primeni neuronskih mreža za modeliranje i simulaciju elektronskih kola*", 3rd Seminar on Neural Network Applications in Electrotechnics, NEUREL-95, 29-30 Sep., 1995, pp. 108-116.
- f.2.* **Litovski, V.**, Aleksić, D., Milenković, S., "*Simulacija računarskih mreža*", Šesnaesti Simp. o novim tehnologijama u poštanskom i telekomunikacionom saobraćaju, Beograd, 1998.
- f.3.* **Litovski, V. B.**, «*Razvoj sekcije "Elektronika" u okviru konferencije ETRANa - prvih 50 godina*», L Konferencija ETRANa, Beograd, Juni 2006. god., pp. EL, 1, 5-8.
- f.4.* Jelena Milojković, Vančo Litovski, "*Predviđanje Maksimuma Dnevne Potrošnje Električne Energije Pomoću Veštačkih Neuronskih Mreža*", Naučno-stručni simpozijum Energetska efikasnost, ENEF 2013, Banja Luka, Nov. 2013, pp. B1-16-B1-21.

5.g. Papers published at international conferences

- g.1. Lazović, S., **Litovski, V.**, "*A New Method Of Band-Pass Allpass Network Synthesis For Compression*", Proc. of the II Int. Symposium on Network Theory, Herceg-Novi, 1971, pp. 275-284.
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	<p>★★★★★ A really good book to read and have !!!, June 30, 2005</p> <p>An excellent book on topics in EDA field.</p> <p>The book is so carefully written that it can inspire its readers to come up with some new circuit simulators.</p> <p>Both analog and digital circuit simulation are covered, and the chapters on matrix treatment, software implementation are specially recommended.</p> <p>What is a little confusing about the book is that the title of "VLSI circuit" somewhat seems to be talking about VLSI digital circuit simulation stuff. However, the book is indeed for study in analog circuit simulation algorithms mostly. The last several chapters do cover some topics in digital circuit simulation.</p> <p>Overall, it is an amazing book to... Read more</p>
<p>VLSI Circuit Simulation and Optimization by V. Litovski</p>	

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10. Reviews of books

1. **Litovski, V.**, "*The VHDL Handbook*", Coehlo, D.R., *J. of Semicustom IC's, Vol. 8, No. 3, Book Review, March, 1991, pp. 53.*
2. **Litovski, V.**, "*VHDL: Hardware Description And Design*", Lipsett, R., Schaefer, C., and Ussery, C., *J. of Semicustom IC's, Vol. 8, No. 3, Book Review, March, 1991, pp. 53.*
3. **Litovski, V.**, Pantić, S., "*The Verilog Hardware Description Language*", Donald E. Thomas and Philip Moorby, *Microelectronics J., Vol.23, No.4, Book review, July, 1992, pp. 316-317.*
4. **Litovski, V.**, Pantić, S., "*Hardware Design And Simulation In VAL/VHDL*", L.M. Augustin, D.C. Luckham, B.A. Gennart, Y. Huk and A.G. Stanculescu, *Microelectronics J., Vol.23, No.4, Book review, July, 1992, pp. 316.*
5. Litovski, V. B., Damnjanović, M., "Discrete events simulation in C", Kevin Watkins, McGraw-Hill, Maidenhead, 1993, *Microelectronics Journal, Vol. 25, No.5, Book review, 1994, pp. 402-403.*

11. Prologue to a book and editing journals and symposia

1. Nančeva Filipova, K., et all., “Ispolzovanie na (v) HDL za sintez na elektronharduer” KING-2001, ISBN 954-9518-21-3.
2. „Electronics“, ISSN 1450-5643, Vol. 8, No. 2, December 2004.
3. Proceedings of the XLVI Conf. of ETRAN, Belgrade 2002, ISBN 86-80509-41-8.



Originals of the books

4. 50 Years of Cooperation Between Faculty of Electronic Engineering in Niš and Faculty of Electrical Engineering in Banja Luka, Plenary Paper, IX Symposium Industrial Electronics INDEL 2012, Banja Luka, November 01_03, 2012.
5. Litovski, V., “Electronics”, in “ET(P)AH, Првих шездесет конференција, Допринос развоју електротехничке струке”, edited by B. Milovanović, B., and Jakšić, Z., Belgrade 2016, Published by ETRAN, pp. 1-10 (in Serbian).



12. List of doctoral students(The text are all in Serbian)

R. B.	Title	Name of the candidate	Subm-itted	Defe-nded
1*	Macromodeling and macroanalysis of CMOS LSI electronic circuits	Predrag Petković		1990
2*	Modeliranje i simulacija defekata u CMOS integrisanim kolima modifikovanom konkurentnom metodom	Dragiša Milovanović		1991
3*	Novi algoritmi za projektovanje veza u integrisanim kolima tipa GEM	Milunka Damnjanović		1991
4	ALECSIS 2.1 – Objektno orijentisani hibridni simulator	Dejan Glozić		1994
5	Dinamičko učenje neuronskih mreža drugog reda zasnovano na simuliranom očvršćavanju	Srđan Milenković		1996
6	Logička simulacija - procena graničnih svojstava projektovanog digitalnog kola	Dejan Maksimović		2000

7	Novi postupci projektovanja i primene mikrokontrolera u automobilskim aplikacijama	Saša Janković		2005
8	Primena veštačkih neuronskih mreža u dijagnostici elektronskih kola	Miona Andrejević Stošović		2006
9	Primena nelinearnog modela idealnog prekidača u simulaciji elektronskih kola	Milan Savić		2007
10	Određivanje statistički najnepovoljnijeg slučaja kašnjenja u digitalnim kolima upotrebom logičkog simulatora	Miljana Sokolović	2007	2009
11	Paralelizacija i gridifikacija simulatora elektronskih kola i sistema sa mešovitim signalima	Bojan Anđelković	2007	x
12	Predviđanje u elektronici pomoću veštačkih neuronskih mreža zasnovano na ograničenoj informaciji	Jelena Milojković	2009	2010
13	Elektronski sistem za analizu polifaznih opterećenja baziran na FPGA	Marko Dimitrijević	2011	2012